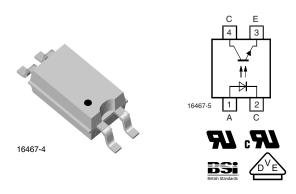


# **TCMT111.** Series

**Vishay Semiconductors** 

## **Optocoupler, Phototransistor Output, Single Channel,** Half Pitch Mini-Flat Package



### DESCRIPTION

The TCMT111X series consist of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin package.

The elements are mounted on one leadframe providing a fixed distance between input and output for highest safety requirements.

### **FEATURES**

- Low profile package (half pitch)
- AC isolation test voltage 3750 V<sub>BMS</sub>
- Low coupling capacitance of typical 0.3 pF
- · Current transfer ratio (CTR) selected into groups
- · Low temperature coefficient of CTR
- Wide ambient temperature range
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

### AGENCY APPROVALS

- UL1577, file no. E76222 system code M
- cUL CSA22.2 bulletin 5A
- DIN EN 60747-5-2 (VDE 0884)
- BSI EN 60065:2002, EN 60950:2000 certificate 8986/8987

### **APPLICATIONS**

- Programmable logic controllers
- Modems
- Answering machines
- · General applications

ORDERING INFO	RMATIC	N								
ТС	M		т	1	1	1	#		SMD-#	h
PART NUMBER							-			
AGENCY CERTIFIED/	AGENCY CERTIFIED/ CTR (%)									
PACKAGE	5 mA	A 10 mA 5 mA								
UL, cUL, BSI, VDE	50 to 600	40 to80	63 to 125	100 to 200	160 to 320	50 to 150	100 to 300	80 to 160	130 to 260	200 to 400
SOP-4	TCMT1110	TCMT1111	TCMT1112	TCMT1113	TCMT1114	TCMT1115	TCMT1116	TCMT1117	TCMT1118	TCMT1119

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION SYMBOL VALUE UNIT								
INPUT									
Reverse voltage		V <sub>R</sub>	6	V					
Forward current		I <sub>F</sub>	50	mA					
Forward surge current	$t_P \le 10 \ \mu s$	I <sub>FSM</sub>	1.5	A					
Power dissipation		P <sub>diss</sub>	80	mW					
Junction temperature		Тj	125	°C					





COMPLIANT



# TCMT111. Series

## **Vishay Semiconductors**

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT					
OUTPUT									
Collector emitter voltage		V <sub>CEO</sub>	70	V					
Emitter collector voltage		V <sub>ECO</sub>	7	V					
Collector current		Ι <sub>C</sub>	50	mA					
Collector peak current	$t_p/T=0.5,t_p\leq 10\ ms$	I <sub>CM</sub>	100	mA					
Power dissipation		P <sub>diss</sub>	150	mW					
Junction temperature		Tj	125	°C					
COUPLER									
AC isolation test voltage (RMS)	Related to standard climate 23/50 DIN 50014	V <sub>ISO</sub>	3750	V <sub>RMS</sub>					
Total power dissipation		P <sub>tot</sub>	230	mW					
Operating ambient temperature range		T <sub>amb</sub>	- 40 to + 110	°C					
Storage temperature range		T <sub>stg</sub>	- 40 to + 125	°C					
Soldering temperature <sup>(1)</sup>		T <sub>sld</sub>	260	С°					

#### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Wave soldering three cycles are allowed. Also refer to "Assembly Instructions" (<u>www.vishay.com/doc?80054</u>).

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb}$ = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT		•						
Forward voltage	I <sub>F</sub> = 5 mA	V <sub>F</sub>		1.15	1.4	V		
Junction capacitance	$V_R = 0, f = 1 MHz$	Cj		50		pF		
OUTPUT		•						
Collector emitter voltage	I <sub>C</sub> = 100 μA	V <sub>CEO</sub>	70			V		
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7			V		
Collector dark current	$V_{CE} = 20 \text{ V}, I_F = 0$	I <sub>CEO</sub>			100	nA		
COUPLER								
Collector emitter saturation voltage	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 1 mA	V <sub>CEsat</sub>		0.1	0.3	V		
Cut-off frequency	$\label{eq:VCE} \begin{array}{l} V_{CE} = 5 \ V, \ I_{F} = 10 \ mA, \\ R_{L} = 100 \ \Omega \end{array}$	f <sub>c</sub>		100		kHz		
Coupling capacitance	f = 1 MHz	C <sub>k</sub>		0.3		pF		

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<b>CURRENT TRANSFER RATIO</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
	$V_{CE} = 5 V, I_F = 5 mA$	TCMT1110	CTR	50		600	%	
		TCMT1111	CTR	40		80	%	
	$V_{CE}$ = 5 V, I <sub>F</sub> = 10 mA	TCMT1112	CTR	63		125	%	
		TCMT1113	CTR	100		200	%	
I <sub>C</sub> /I <sub>F</sub>		TCMT1114	CTR	160		320	%	
IC/IF	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 5 mA	TCMT1115	CTR	50		150	%	
		TCMT1116	CTR	100		300	%	
		TCMT1117	CTR	80		160	%	
		TCMT1118	CTR	130		260	%	
		TCMT1119	CTR	200		400	%	

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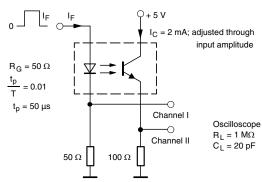
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# TCMT111. Series

## Vishay Semiconductors

SWITCHING CHARACTERISTICS ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Delay time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 1) \end{array}$	t <sub>d</sub>		3		μs		
Rise time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 1) \end{array}$	t <sub>r</sub>		3		μs		
Fall time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 1) \end{array}$	t <sub>f</sub>		4		μs		
Storage time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 1) \end{array}$	ts		0		μs		
Turn-on time	$\label{eq:VS} \begin{array}{l} V_S = 5 \; V, \; I_C = 2 \; mA, \; R_L = 100 \; \Omega, \\ (\text{see figure 1}) \end{array}$	t <sub>on</sub>		5		μs		
Turn-off time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 1) \end{array}$	t <sub>off</sub>		3		μs		
Turn-on time	$V_S = 5 \text{ V}, \text{ I}_F = 10 \text{ mA}, \text{ R}_L = 1 \text{ k}\Omega,$ (see figure 2)	t <sub>on</sub>		9		μs		
Turn-off time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_F = 10 \ mA, \ R_L = 1 \ k\Omega, \\ (see \ figure \ 2) \end{array}$	t <sub>off</sub>		18		μs		



١<sub>F</sub> 1 ٥ t<sub>n</sub> t  $\mathsf{I}_{\mathsf{C}}$ 100 % 90 % 10 % 0 t tf t off  $egin{aligned} t_p \ t_d \ t_r \ t_{on} \ (= t_d + t_r) \end{aligned}$ Pulse duration Storage time t, Delay time Rise time Fall time Turn-off time t  $\dot{t_{off}} (= t_s + t_f)$ Turn-on time 96 11698 Fig. 3 - Switching Times

95 10804

Fig. 1 - Test Circuit, Non-Saturated Operation

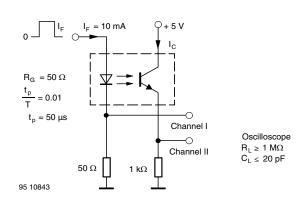


Fig. 2 - Test Circuit, Saturated Operation

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# TCMT111. Series

### **Vishay Semiconductors**

SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification	IEC 68 part 1			40/110/21				
Comparative tracking index		CTI	175		399			
Peak transient overvoltage		V <sub>IOTM</sub>	6000			V		
Peak insulation voltage		VIORM	707			V		
Safety rating - power output		P <sub>SO</sub>			265	mW		
Safety rating - input current		I <sub>SI</sub>			130	mA		
Safety rating - temperature		T <sub>SI</sub>			150	°C		
Creepage distance			5			mm		
Clearance distance			5			mm		
Insulation thickness, reinforced rated	per IEC 60950 2.10.5.1		0.4			mm		

Note

As per IEC 60747-5-2, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



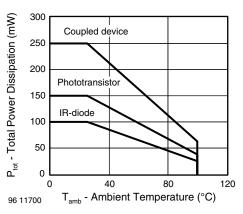


Fig. 4 - Total Power Dissipation vs. Ambient Temperature

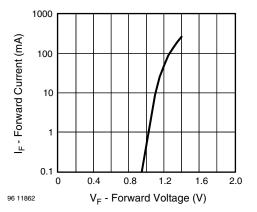
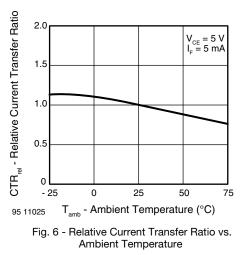


Fig. 5 - Forward Current vs. Forward Voltage



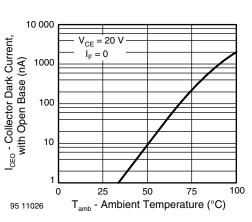


Fig. 7 - Collector Dark Current vs. Ambient Temperature

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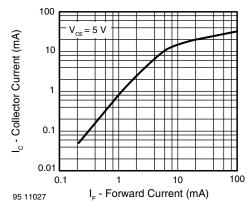


Fig. 8 - Collector Current vs. Forward Current

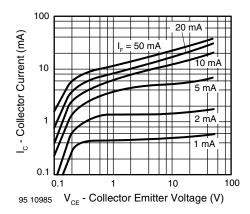


Fig. 9 - Collector Current vs. Collector Emitter Voltage

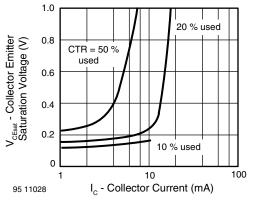


Fig. 10 - Collector Emitter Saturation Voltage vs. Collector Current

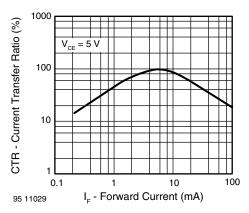


Fig. 11 - Current Transfer Ratio vs. Forward Current

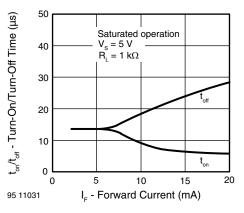


Fig. 12 - Turm-on/off Time vs. Forward Current

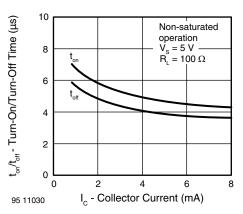


Fig. 13 - Turn-on/off Time vs. Collector Current

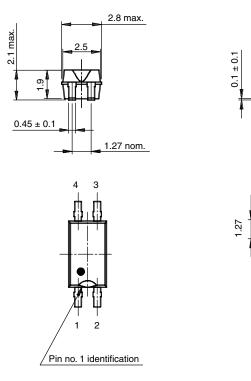
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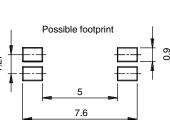


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Vishay Semiconductors

### **PACKAGE DIMENSIONS** in millimeters





7 + 0.3

 $4.4 \pm 0.2$ 

0.7 + 0.3

+ 0.10 - 0.05

0.15



16283

PACKAGE MARKING (example of TCMT1110)





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